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Comments

BTV.255986.1

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Ellis et al. Serial No.: 10/707,797

Filed: January 13, 2004

Title: Integrated Redundancy Architecture

And Method for Providing Redundancy Allocation to an Embedded Memory

System

Group Art Unit: Not yet assigned

Examiner: Not yet assigned

Attorney Docket No.: BUR920030151US1

(02016-00170)

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

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January 21, 2004

Transmittal of Preliminary Amendment

Transmitted herewith for filing is a Preliminary Amendment to the above-identified Application.

No additional fee is required.

The fee has been calculated as shown below:

P248-10/03

Page 1 of 2

Law Offices of
Downs Rachlin Martin PLLC
199 Main Street
P.O. Box 190
Burlington, VT 05402-0190
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CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NO. PREVIOUSLY PAID FOR	PRESENT EXTRA	RATE	ADDITIONAL FEE
TOTAL 20	MINUS	20	= 0	x \$18.00/ \$9.00 =	\$00.00
INDEPENDENT 3	MINUS	3	= 0	x \$86.00/ \$43.00 =	\$00.00
1ST PRESENTATION OF MULTIPLE DEP. CLAIMS			\$290.00/ \$145.00 =	\$00.00	
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	Any additional filing fees required under 37 C.F.R. §1.16.			
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Respectfully submitted,
DOWNS RACHLIN MARTIN PLLC

Anthony F. Gangemi Attorney of Record

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BTV.258834.1

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1/21/2

January 21, 2004

PRELIMINARY AMENDMENT

This Preliminary Amendment is being filed within three months of the filing date, i.e., January 13, 2004, of the present application. Accordingly, entry of this Preliminary Amendment prior to examining the present application is requested per 37 C.F.R. § 1.115(b)(2)(i).